

VALVE ELECTRONIC

CV2260

GENERAL POST OFFICE: E-IN-C (S)

Specification: G.P.C./CV2260/Issue 2 Dated: January, 1957 To be read in conjunction with K 1001 ignoring Clause 5.2	<u>SECURITY</u>	
	<u>Specification</u> Unclassified	<u>Valve</u> Unclassified

→ indicates a change

<u>TYPE OF VALVE:</u> Sub-miniature pentode		<u>MARKING</u> CV 2260 Code date of manufacture Factory identification code.	
<u>CATHODE:</u> Directly heated		<u>BASE</u> B5A (see drawing on page 3)	
<u>ENVELOPE:</u> Unmetallised glass			
<u>PROTOTYPE</u> C2DF		<u>CONNECTIONS</u> (see drawing on page 3)	
<u>RATING</u>		<u>NOTE</u>	
Filament voltage (V)	0.625	A A A	
Nominal filament current (ma)	10.0		
Max. anode voltage (V)	45.0		
Max. screen voltage (V)	45.0		
Mutual conductance ($\mu\text{A}/\text{V}$)	100.0		
Anode impedance (megohms)	1.0		
Max. cathode current (μA)	75.	<u>DIMENSIONS</u> (see drawing on page 3)	
<u>CAPACITANCES</u> (pF)		<u>NOTE</u> A. Measured with $V_a = V_{g2} = 15$ and $V_{g1} = -0.625$ A sharp bend must not be made in any valve lead closer than 1.5 mm to the glass seal and soldered joints in the leads must not be made closer than 5.0 mm to the seal.	
(Unscreened)			
C ag	0.2		
C in	1.7		
C out	2.4		

Z.15611.R.

CV 2260/2/1

To be performed in addition to those applicable in K 1001

TEST CONDITIONS						LIMITS		No. Tested
	Vf	Vht	f(c/s)	Vg1		Min.	Max.	
a	0.625	-	-	-	If (mA)	9.0	11.0	100%
→ b	0.55	14	50	-	Gain (Note 2) (db)	25		100%
c	0.55	14	50	-	Gain (Note 2) (db)	Note 4		Sampling Test
d	0.55	14	1000	-	Gain (Note 2) (db)	Note 5		Sampling Test
→ e	0.75	14	50	-	Gain (Note 2) (db)	25		10 per week
→ f	0.55	14	50	0.75	Gain (Note 2) (db)	18		10 per week
→ g	0.75	18	50	-	Gain (Note 2) (db)	27		10 per week
h	0.75	18	-	-	Microphony (Note 3)		Note 3	100%

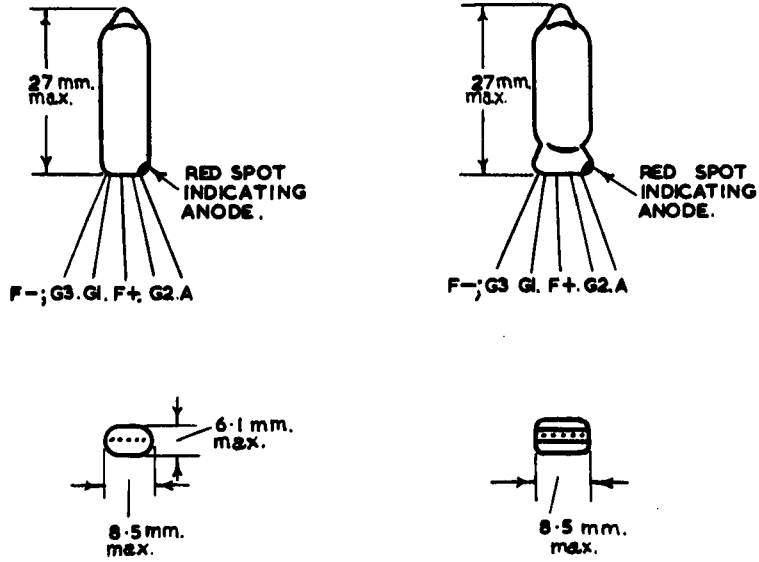
NOTES

1. The equipment used for testing is to be approved by G.P.O.
2. Tested in Test Circuit shown on page 4.
3. The input terminals of the test circuit shown on page 4 shall be short circuited and the output terminals shall be connected to an amplifier having an input impedance greater than 50 megohms. The amplifier shall have a gain which does not vary more than ± 2 db over a frequency range from 800 to 4,000 c.p.s. but cuts off sharply above 4,000 c.p.s. The response may fall by 3 db at 600 c.p.s. but not more than 6 db per octave below that. The output of the amplifier shall be connected to a meter having a movement with a period of not less than 3 seconds and not greater than 5 seconds and an over-swing of approximately 15% when connected to the amplifier. The amplifier shall have such a gain that an input of 100 mV. R.M.S. at approximately 1,000 c.p.s. will give a full scale deflection on the output meter.

The valve shall be tested for microphony by tapping it steadily with a rubber headed mallet at a rate of approximately 5 times per second in such a direction and position on the valve that the greatest possible sustained microphony response is excited as indicated on the meter. The valve shall be rejected if the meter reading can be maintained at a value exceeding that corresponding to an input of 25 mV R.M.S. at approximately 1,000 c.p.s. (A suggested mallet for tapping the valve is an ordinary lead pencil fitted at one end with a sleeve of india rubber approximately $\frac{1}{2}$ " thick).

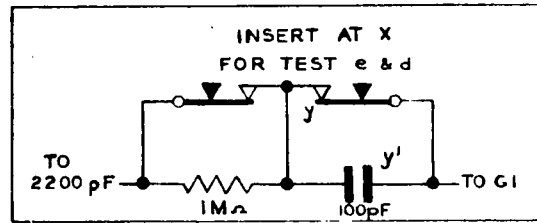
4. With 1.0 Megohms inserted in series with 2,200 pF capacitance in input circuit, the gain to be within 1.0 db of the gain obtained in Test b.
5. With 100 pF inserted in series with 2,200 pF capacitance in input circuit, the gain to be within 2.0 db of the gain obtained without the 100 pF inserted.

PIN CONNEXIONS & OUTLINE DRAWING.

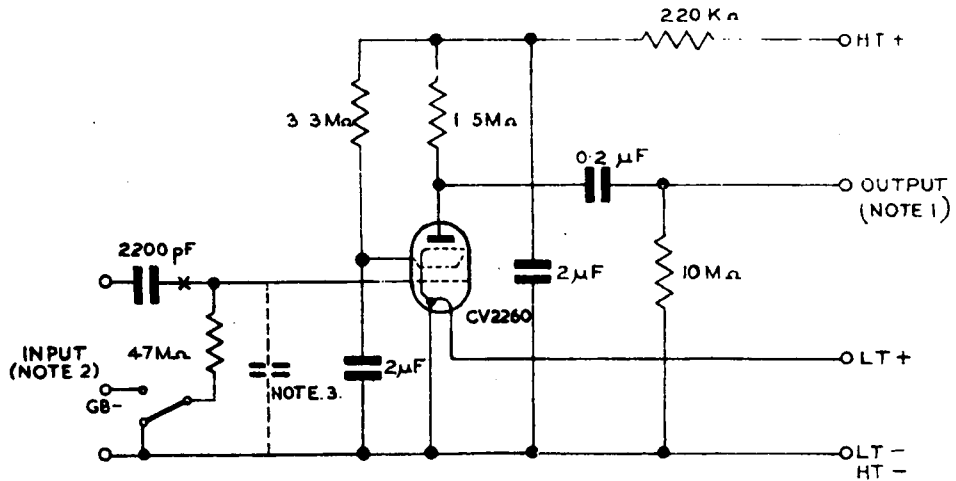


SPACING OF LEADS 1.3 mm.

THE LEADS SHALL BE FLEXIBLE TINNED, COPPER CLAD NICKEL IRON WIRE, OF 0.34-0.48mm DIAMETER AND AT LEAST 32mm. IN LENGTH.



TEST CIRCUIT



- NOTES:-
1. OUTPUT IS MEASURED BETWEEN OUTPUT TERMINAL & HT- BY MEANS OF A HIGH IMPEDANCE FULL WAVE VOLTMETER INDICATING AVERAGE VALUES.
 2. INPUT NOT GREATER THAN 50 mV.
 3. FOR TEST d THE STRAY CAPACITANCE, SHOWN DOTTED, WITH VALVE REMOVED AND CIRCUIT BROKEN AT y & y' (SEE INSET) TO BE BUILT UP TO 12 pF